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Analog Devices' High-Resolution, Low-Power SAR ADC Ensures Shorter Test Times and Product Development Cycles

NORWOOD, Mass.--(BUSINESS WIRE)-- [Analog Devices, Inc.](http://www.analog.com) today announced a new generation of high-precision successive-approximation (SAR) analog-to-digital converters (ADCs) that uniquely combine the attributes of high performance, low power, small footprint, and ease of use. These ICs enable mobile test and measurement instruments to operate for longer duration during field tests while improving measurement accuracy and repeatability. The new devices also support development of smaller-sized instruments, which can be placed closer to the sensors being measured, or allow for an increased number of data acquisition channels in the same form factor. An instrument with these characteristics will enhance the efficiency of field testing and reduce the costs associated with new product characterization time.

This Smart News Release features multimedia. View the full release here:

<http://www.businesswire.com/news/home/20161103005006/en/>



View product page, download data sheet, order samples and evaluation boards:

<http://www.analog.com/AD4003> or

<http://www.analog.com/AD4000>

Connect with engineers and experts on EngineerZone®, Analog Devices' online technical support community:

Analog Devices' High-Resolution, Low-Power SAR ADC Ensures Shorter Test Times and Product Development Cycles (Photo: Business Wire)

https://ez.analog.com/community/data_converters

The high performance and ease of use of the 2-MSPS, 18-bit AD4003 and 16-bit AD4000 SAR ADCs greatly ease the efforts of system designers tasked with achieving optimum performance in data acquisition designs within a shorter design cycle, and without the need for making often difficult and conflicting technical tradeoffs. Features such as high input-impedance mode and span-compression mode reduce the design challenge associated with the ADC driver stage and increase the flexibility of amplifier selection. The high input-impedance mode allows the use of low-power precision amplifiers to directly drive the ADC, and reduces the signal-chain power demands. In addition, the internal overvoltage protection removes the need for external protection devices, and the span compression enables the ADC driver stage to operate from the same supply rail as the ADC, thus simplifying power management. This combination supports increased channel density

while lowering the system-level power requirements, without compromising performance.

The AD400x series includes 20-, 18-, and 16-bit SAR ADCs with speed options from 500 kSPS to 2 MSPS.

Pricing and Availability

Product	Resolution	Sampling Rate	Signal-Noise Ratio	Integral Non Linearity	Price (1,000 units)	Package Options	Full Production
AD4003	18 Bits	2 MSPS	100.5 dB	±0.5 LSB	\$19.85	10-pin 3 × 4.9 mm MSOP 10-pin 3 × 3 mm LFCSP	Now
AD4000	16 Bits	2 MSPS	93 dB	±0.2 LSB	\$12.75	10-pin 3 × 4.9 mm MSOP 10-pin 3 × 3 mm LFCSP	Now

About Analog Devices

Analog Devices (NASDAQ: ADI) designs and manufactures semiconductor products and solutions. We enable our customers to interpret the world around us by intelligently bridging the physical and digital with unmatched technologies that sense, measure and connect. Visit <http://www.analog.com>.

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Read and subscribe to *Analog Dialogue*, ADI's monthly technical journal, at: <http://www.analog.com/analog-dialogue.html>

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