

## Analog Devices' RadioVerse® SoC Drives 5G Radio Efficiency and Performance

December 9, 2021

WILMINGTON, Mass.--(BUSINESS WIRE)--Dec. 9, 2021-- <u>Analog Devices</u>, Inc. (Nasdaq: ADI) today announced a breakthrough RadioVerse<sup>®</sup> System-on-Chip (SoC) series providing radio unit (RU) developers with an agile and cost-effective platform to create the most energy efficient 5G RUs in the industry. The new SoC series provides advanced RF signal processing with expanded digital functionality and RF capacity that greatly improves 5G RU performance and energy efficiency. The SoCs are the newest addition to ADI's RadioVerse ecosystem and combine its award-winning Zero IF (ZiF) architecture with significant advances in functional integration and linearization. ADI's RadioVerse devices are the most widely used software-defined transceivers in 4G and 5G RUs worldwide.<sup>1</sup>

This press release features multimedia. View the full release here: https://www.businesswire.com/news/home/20211209005125/en/



Analog Devices' RadioVerse™ SoC Drives 5G Radio Efficiency and Performance (Graphic: Business Wire)

"Samsung and ADI have long worked together to support the swift deployment of 5G in the global market," said Dong Geun Lee, Vice President and Head of Hardware R&D Group, Network Business at Samsung Electronics. "We are excited for the successful launch of ADI's new SoC, as we expect this cutting-edge technology will bring better 5G experience to consumers. We look forward to expanding our engagement with ADI."

Demand for power efficient RUs is expanding rapidly as global network operators race to deploy 5G infrastructure. With the exponential growth of wireless demand, energy efficiency is a key metric for operators as they seek to reduce their carbon footprint while expanding network capacity. The new RadioVerse SoC series requires very low power compared to alternatives and implements advanced algorithms that deliver optimal RU system efficiency.

"RadioVerse SoCs are designed to optimize the full radio solution rather than just a single component or interface," said Joe Barry, Vice President of Wireless Communications at Analog Devices. "Each successive generation has provided expanded capabilities, bandwidth and performance, while improving overall RU efficiency. This new RadioVerse SoC series takes a big step forward by delivering multiple advancements in signal processing to meet the demanding needs of 5G."

The ADRV9040 is the first in the new RadioVerse SoC series. It offers eight

transmit and receive channels of 400MHz bandwidth and integrates advanced digital signal processing functions, including carrier digital up-converters (CDUC), carrier digital down-converters (CDUC), crest factor reduction (CFR) and digital pre-distortion (DPD). This expanded signal processing can eliminate the need for a field-programmable gate array (FPGA), thereby reducing thermal footprint, and total system size, weight, power, and cost. The SoC's DPD algorithms were developed using advanced machine learning techniques and are optimized in close collaboration with major power amplifier (PA) vendors to ease the design burden and deliver best-in-class wide bandwidth performance. The algorithms are fully tested and validated across 4G and 5G use cases, including various PA technology types such as gallium nitride (GaN). In addition, the ZiF radio

architecture simplifies RF filtering and signal chain components, reducing RU cost and development time for band and power variants designs.

• Learn more about the ADRV9040 RadioVerse SoC at http://www.analog.com/ADRV9040

## **About Analog Devices**

Analog Devices, Inc. (Nasdaq: ADI) operates at the center of the modern digital economy, converting real-world phenomena into actionable insight with its comprehensive suite of analog and mixed signal, power management, radio frequency (RF), and digital and sensor technologies. ADI serves 125,000 customers worldwide with more than 75,000 products in the industrial, communications, automotive, and consumer markets. ADI is headquartered in Wilmington, MA. Visit <a href="http://www.analog.com">http://www.analog.com</a>.

<sup>1</sup>Source: Mobile Experts Semiconductors for RRH 2021

All trademarks and registered trademarks are the property of their respective owners.

(ADI-WEB)

View source version on businesswire.com: https://www.businesswire.com/news/home/20211209005125/en/

Linda Kincaid Analog Devices, Inc. linda.kincaid@analog.com

Source: Analog Devices, Inc.